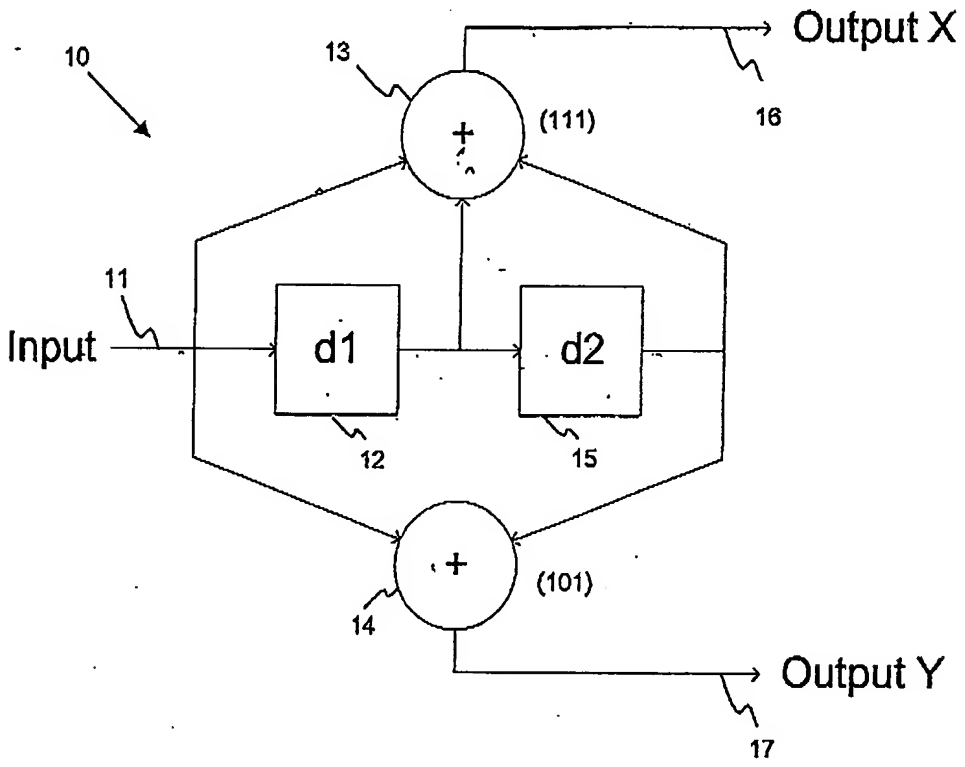
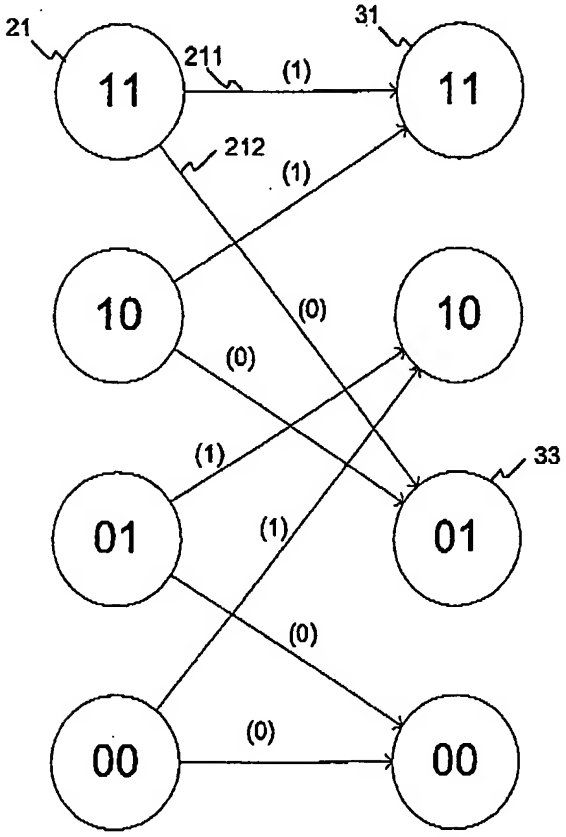
Rate 1/2 Convolutional encoder, constraint length = 3



PRIOR ART

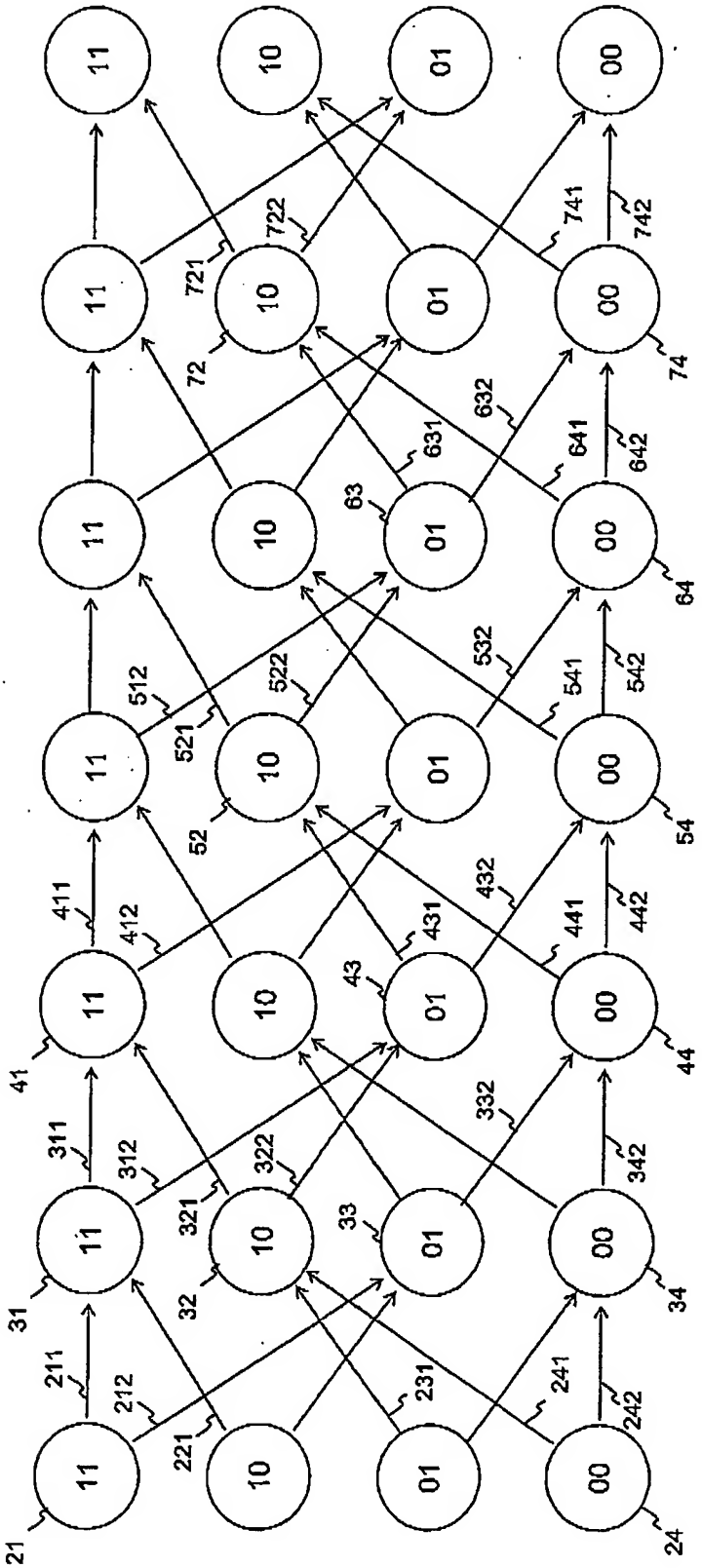
Fig. 1

4 State Viterbi decoder trellis



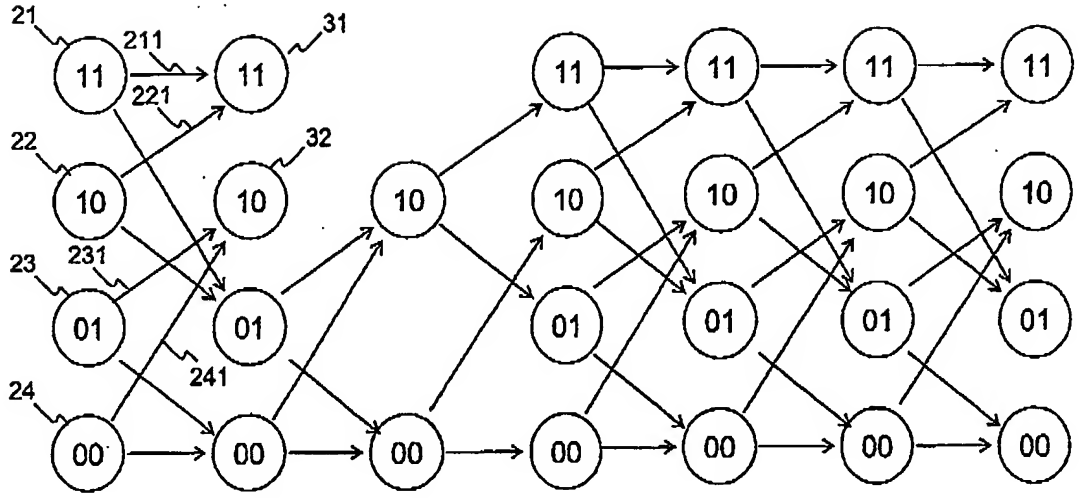
PRIOR ART

Fig. 2



PRIOR ART

Fig. 3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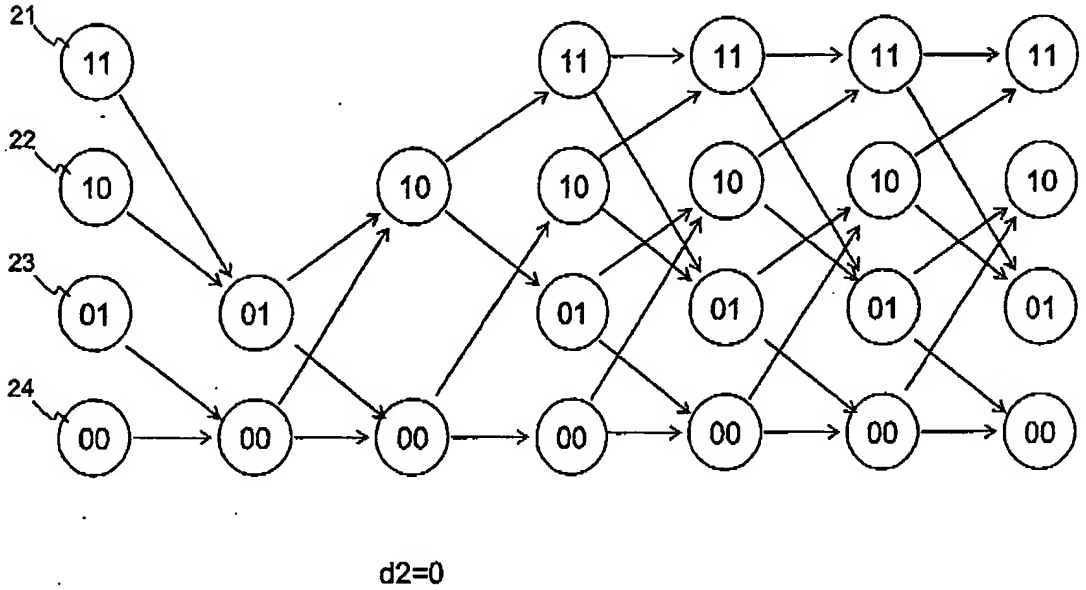
211
31
11
11
221
22
32
10
10
10
10
10
10
10
23
231
01
01
01
01
01
01
241
24
00
00
00
00
00
00
00
d2=0

Fig. 4A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11
11
11
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22
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10
10
10
10
10
10
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01
01
01
01
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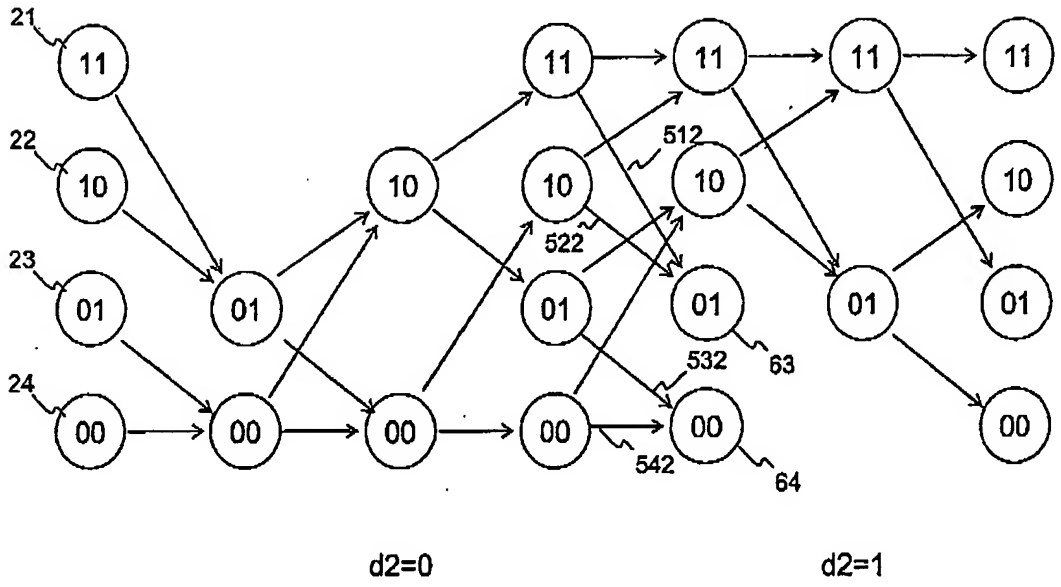
00
00
00
00
00
00
d2=0

Fig. 4B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22
23
24
11
10
01
00
512
522
532
542
63
64
d2=0
d2=1

Fig. 5A

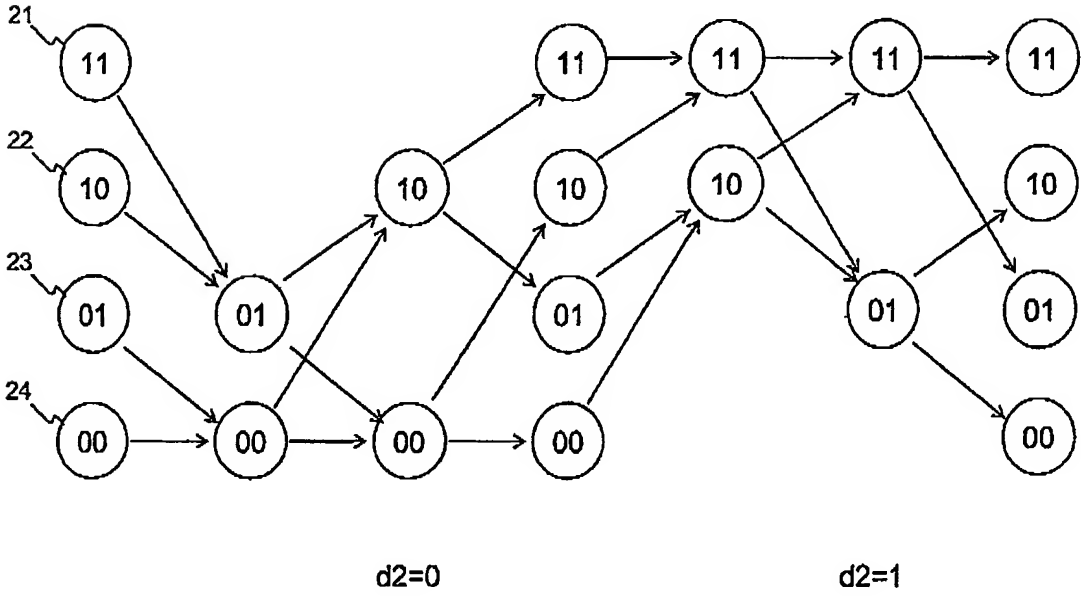

21
22
23
24
11
10
01
00
d2=0
d2=1

Fig. 5B

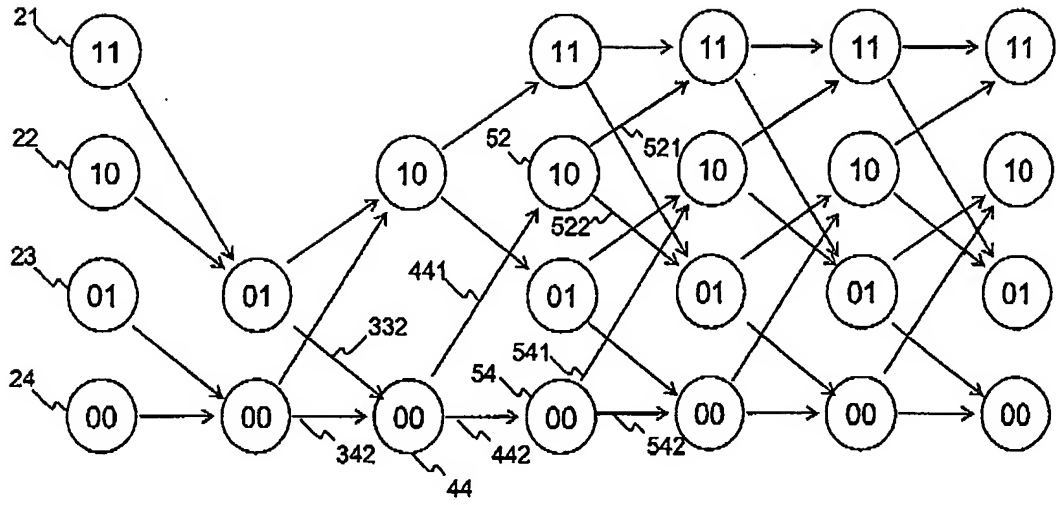

21
22
23
24
11
10
01
00
52
521
522
441
332
541
54
342
442
542
44
d2=0
d2=1

Fig. 6A

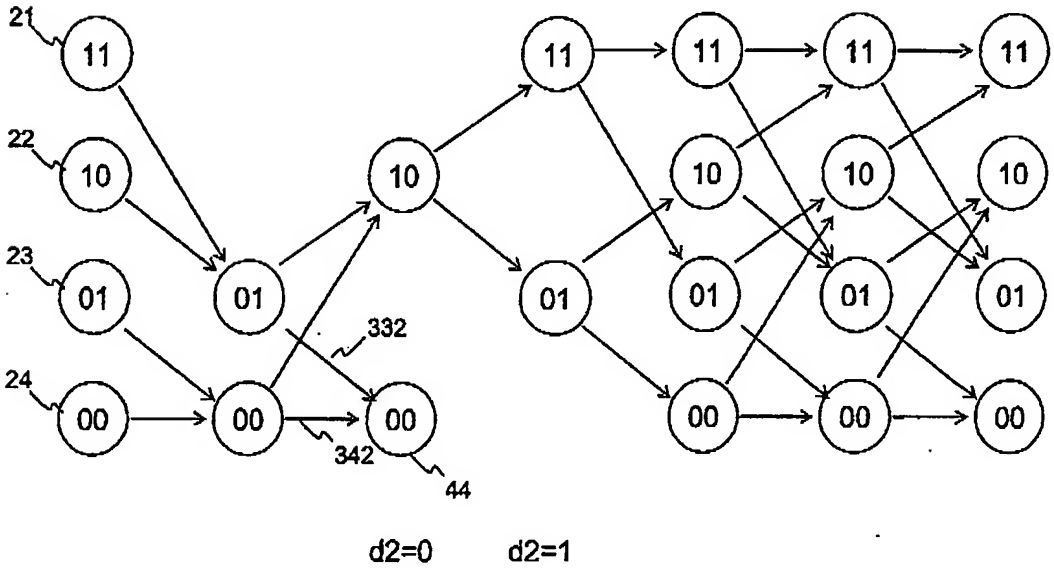

21
22
23
24
11
10
01
00
332
342
44
d2=0
d2=1

Fig. 6B

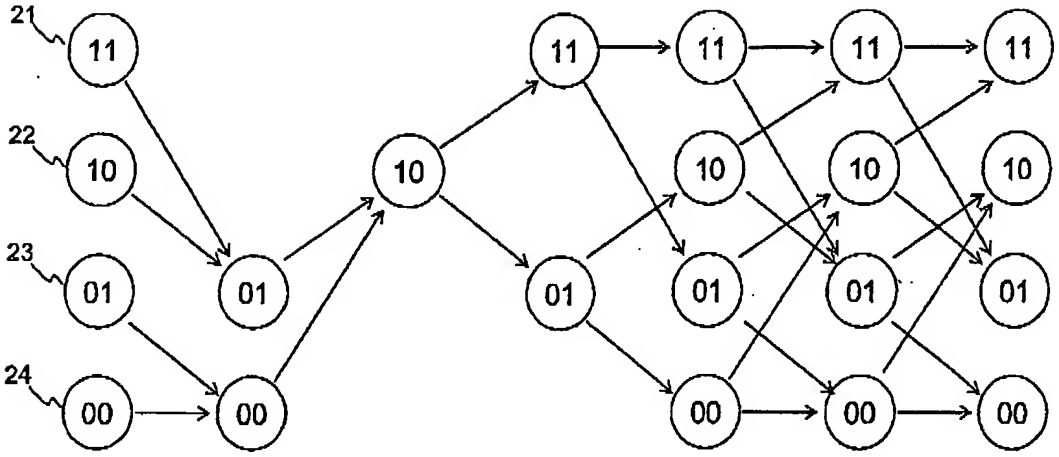


Fig. 6C

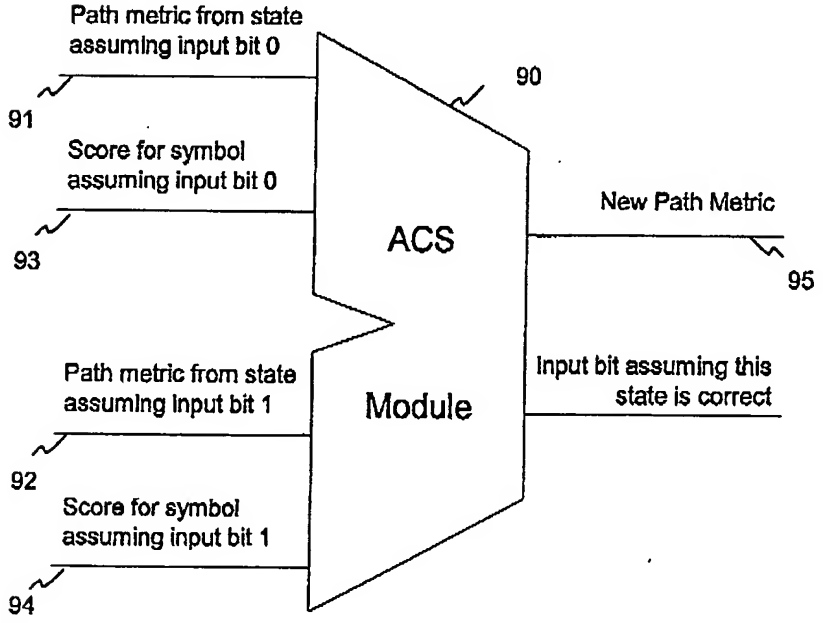
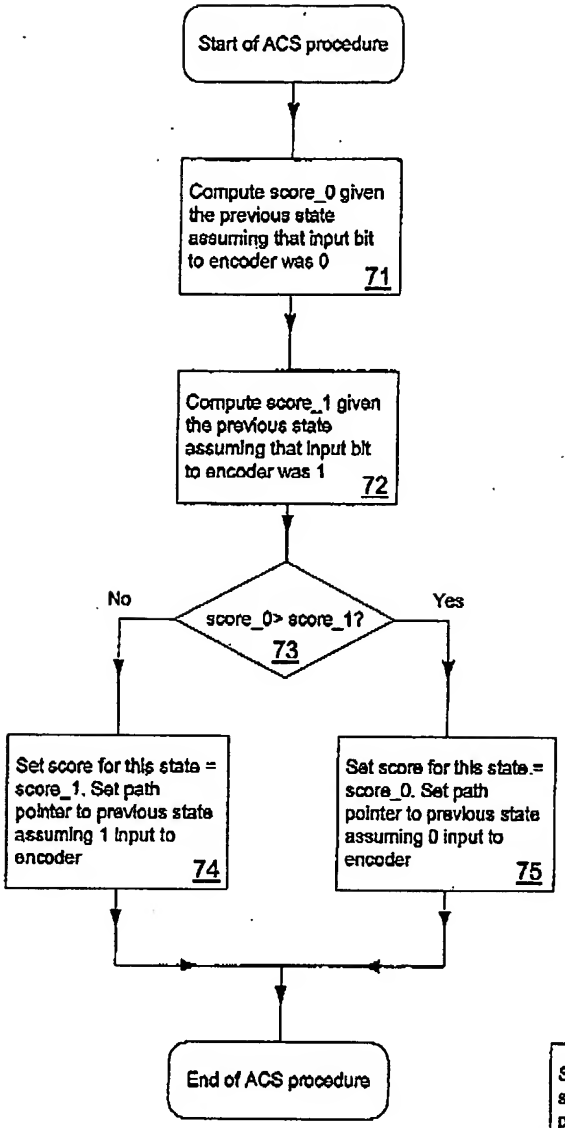


Fig. 9

PRIOR ART



PRIOR ART

Fig. 7

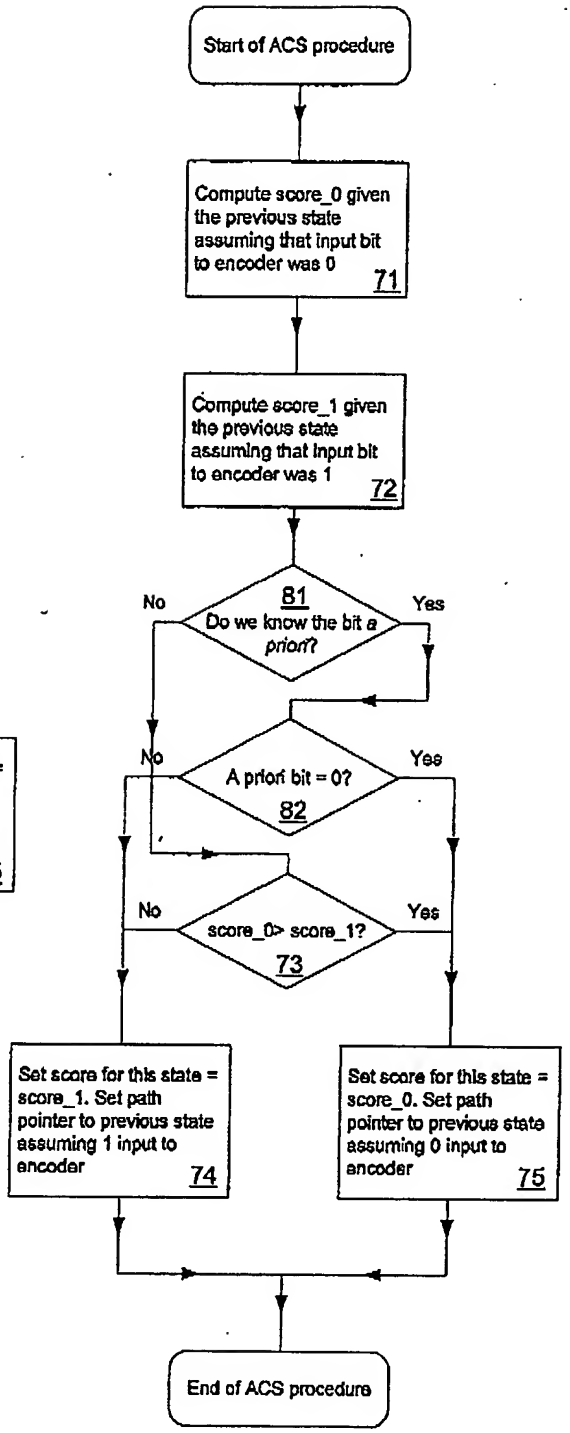


Fig. 8

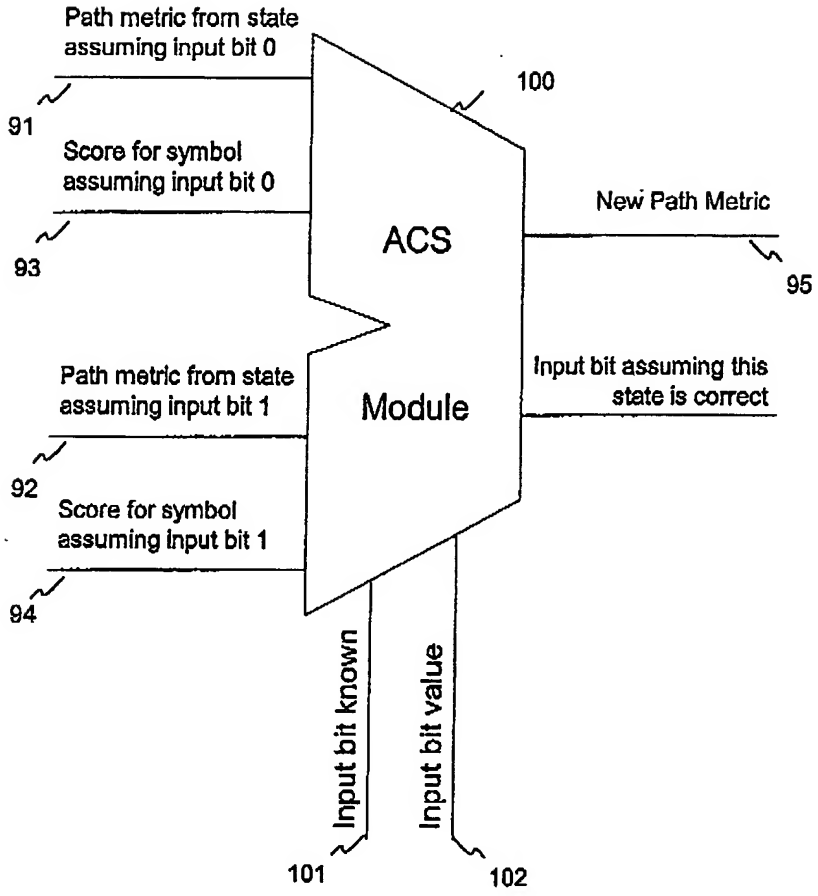

Path metric from state assuming input bit 0
91
Score for symbol assuming input bit 0
93
Path metric from state assuming input bit 1
92
Score for symbol assuming input bit 1
94
100
ACS
Module
New Path Metric
95
Input bit assuming this state is correct
Input bit known
101
Input bit value
102

Fig. 10